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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO
09/944,963	08/31/2001	Sarathy Sribhashyam	51185.00002	2222
30256	7590 09/14/2005		EXAMINER	
SQUIRE, SANDERS & DEMPSEY L.L.P			TRA, ANH QUAN	
600 HANSEN WAY PALO ALTO, CA 94304-1043			ART UNIT	PAPER NUMBER
			2816	

DATE MAILED: 09/14/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

	Application No.	Applicant(s)			
	09/944,963	SRIBHASHYAM ET AL.			
Office Action Summary	Examiner	Art Unit			
	Quan Tra	2816			
The MAILING DATE of this communication app Period for Reply	ears on the cover sheet with the c	orrespondence address			
A SHORTENED STATUTORY PERIOD FOR REPLY THE MAILING DATE OF THIS COMMUNICATION. - Extensions of time may be available under the provisions of 37 CFR 1.13 after SIX (6) MONTHS from the mailing date of this communication. - If the period for reply specified above is less than thirty (30) days, a reply - If NO period for reply is specified above, the maximum statutory period w - Failure to reply within the set or extended period for reply will, by statute, Any reply received by the Office later than three months after the mailing earned patent term adjustment. See 37 CFR 1.704(b).	36(a). In no event, however, may a reply be timed within the statutory minimum of thirty (30) days will apply and will expire SIX (6) MONTHS from the cause the application to become ABANDONED	ely filed will be considered timely. the mailing date of this communication. O (35 U.S.C. § 133).			
Status		·			
1) Responsive to communication(s) filed on 08 A	<u>ugust 2005</u> .				
2a) This action is FINAL . 2b) ⊠ This	This action is FINAL . 2b)⊠ This action is non-final.				
3) Since this application is in condition for allowance except for formal matters, prosecution as to the ments is closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.					
Disposition of Claims	•				
4) ☐ Claim(s) 1-16 is/are pending in the application. 4a) Of the above claim(s) 10-16 is/are withdraw 5) ☐ Claim(s) is/are allowed. 6) ☐ Claim(s) 1-9 is/are rejected. 7) ☐ Claim(s) is/are objected to. 8) ☐ Claim(s) are subject to restriction and/or	n from consideration.	·			
Application Papers					
9)☐ The specification is objected to by the Examiner.					
10)☐ The drawing(s) filed on is/are: a)☐ accepted or b)☐ objected to by the Examiner.					
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).					
Replacement drawing sheet(s) including the correction 11) The oath or declaration is objected to by the Ex					
Priority under 35 U.S.C. § 119					
12) Acknowledgment is made of a claim for foreign a) All b) Some * c) None of: 1. Certified copies of the priority documents 2. Certified copies of the priority documents 3. Copies of the certified copies of the priority application from the International Bureau * See the attached detailed Office action for a list of	s have been received. s have been received in Application ity documents have been receive I (PCT Rule 17.2(a)).	on No d in this National Stage			
Attachment(s)	_				
1) Notice of References Cited (PTO-892) 2) Notice of Draftsperson's Patent Drawing Review (PTO-948)	4)				
3) Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08) Paper No(s)/Mail Date		atent Application (PTO-152)			

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DETAILED ACTION

Election/Restrictions

1. Applicant's election without traverse of Group I, claims 1-9 in the reply filed on 08/08/05 is acknowledged.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless -

- (b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.
- 3. Claims 1, 2 and 4-9 are rejected under 35 U.S.C. 102(b) as being anticipated by Kim (USP 6359481).

As to claim 1, Kim discloses in figure 3 shows an apparatus for compensating for glitch occurrence reset signal that is applied in an integrated circuit, the apparatus comprising: a logic stage capable process an incoming signal (input) and a delayed incoming signal (ND) that delayed version of the incoming signal, the logic stage capable generate an output signal (output) so that when the incoming signal and delayed incoming signal are in the same state, the output signal will be in the same state.

As to claim 2, figure 3 shows that the logic stage comprises an S-R flip flop (G6, G8).

As to claim 4, figure 3 shows that the logic stage comprises a NAND gate (G2) capable to receive incoming signal and delayed incoming signal; and an OR gate (G4) capable to receive the incoming signal and the delayed incoming signal.

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As to claim 5, figure 3 shows that the delayed incoming signal is delayed by a delay stage.

As to claim 6, figure 3 shows that the logic portion is capable at least first glitch occurring before a high state portion of the incoming signal and a second glitch occurring after the high state portion of the incoming signal.

Claims 7-9 recite similar limitations of claims above. Therefore, they are rejected for the same reasons.

Claim Rejections - 35 USC § 103

- 4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 5. Claim 3 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kim (USP 6359481).

Kim's figure 3 fails to show a NOR gate and an inverter connected in series to the NOR gate. However, it is notoriously well known in the art that OR gate is made by NOR gate connected in series to an inverter. Therefore, it would have been obvious to one having ordinary skill in the art to use NOR gate connected in series to an inverter for Kim OR gate (G4) due to doctrine equivalent of function.

Conclusion

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Any inquiry concerning this communication or earlier communications from the examiner should be directed to Quan Tra whose telephone number is 571-272-1755. The examiner can normally be reached on 8:00 A.M.-5:00 P.M..

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Timothy Callahan can be reached on 571-272-1740. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

QUAN TRA PRIMARY EXAMINER

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September 13, 2005